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	Filing Date		2001-11-01	
	First Named Inventor	Craig	Nemecek	
	Art Unit		2123	
	Examiner Name	Jason	Scott Proctor	
	Attorney Docket Number	er	CD01213M	

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	1	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages					
	2	USPTO Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al., filed on 11/19/2001; 62 pages					
	3	USPTO Notice of Allowance for Application Number 10/024,093 (CD99069C) dated 09/10/2002; 7 pages					
	4	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 06/11/2010; 12 pages					
	5	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 07/21/2010; 3 pages					
	6	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 1 page					
USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages							
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